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**PULSE PERIOD DECISION DEVICE**

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**International Class (IPC Edition 5):**

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**JAPIO Class:**

- 46.1 (INSTRUMENTATION--- Measurement)

**Abstract:**

**PURPOSE:** To simplify the software of a CPU by realizing hysteresis for preventing hunting for the period decision making of an input pulse by hardware.

**CONSTITUTION:** Retriggerable one-shot multivibrators 1 and 2 are triggered with input pulses whose periods are to be decided as negative edge trigger pulses and timer values  $T(\text{sub } 1)$  and  $T(\text{sub } 2) (< T(\text{sub } 1))$  are determined by resistance  $R(\text{sub } 1)$  and capacity  $C(\text{sub } 1)$ . The terminal  $Q(\text{sub } 1)$  of the vibrator 1 and the terminal Q of a D latch 5 are connected to the input terminal of an AND gate 3 and the output terminal of the gate 3 and the terminal  $Q(\text{sub } 2)$  of the vibrator 2 are connected to the input terminal of an OR gate 4. The output terminal of the gate 4 is connected to a D terminal and the latch 5 is triggered with the input pulse whose period T is to be decided as the negative edge trigger pulse to output a decision signal from its Q terminal. Consequently, the period T of the input pulse can be decided with hysteresis of time width  $(T(\text{sub } 1) - T(\text{sub } 2))$  by setting the timer values  $T(\text{sub } 1)$  and  $T(\text{sub } 2)$  of the multivibrators 1 and 2 respectively. (From: *Patent Abstracts of Japan*, Section: P, Section No. 1043, Vol. 14, No. 207, Pg. 44, April 26, 1990 )